

DENON

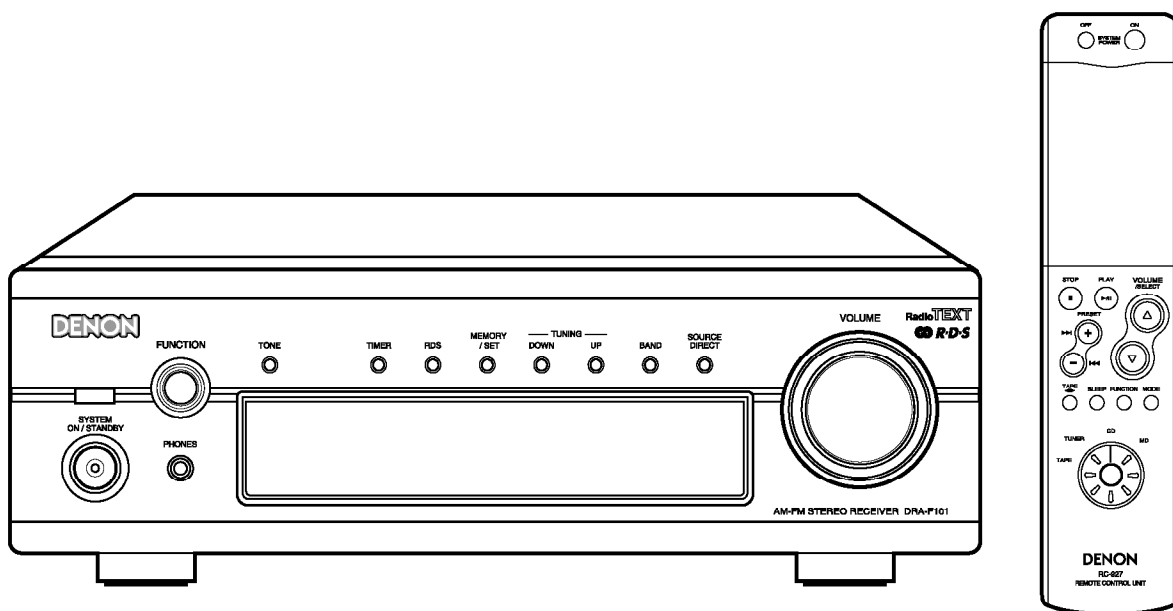
For Europe & U.K. model

Hi-Fi AM-FM Stereo Receiver

SERVICE MANUAL

MODEL DRA-F101

AM-FM STEREO RECEIVER



• Some illustrations using in this service manual are slightly different from the actual set.

DENON, Ltd.

16-11, YUSHIMA 3-CHOME, BUNKYOU-KU, TOKYO 113-0034 JAPAN
Telephone: 03 (3837) 5321

SAFETY PRECAUTIONS

The following check should be performed for the continued protection of the customer and service technician.

LEAKAGE CURRENT CHECK

Before returning the unit to the customer, make sure you make either (1) a leakage current check or (2) a line to chassis resistance check. If the leakage current exceeds 0.5 milliamps, or if the resistance from chassis to either side of the power cord is less than 460 kohms, the unit is defective.

SPECIFICATIONS

• POWER AMPLIFIER SECTION

Rated Output Power: 35W + 35W (4 Ω /ohms, DIN, 1 kHz, T.H.D. 0.7%)
Output terminals: 4 to 16 Ω /ohms

• PRE AMPLIFIER SECTION

PHONO EQ Amp Rated Output: 150 mV (Recout Terminal)
Input Sensitivity/Input Impedance: PHONO: 2.5 mV/47 k Ω /ohms
 CD, DVD/AUX, TAPE, MD: 300 mV/47 k Ω /ohms
RIAA Deviation: PHONO: 20 HZ to 20 kHz \pm 0.5 dB

• OVERALL CHARACTERISTICS

SN Ratio (IHF A Network): PHONO: 80 dB (at 5 mV input) (input terminals shout-circuited)
 CD, DVD/AUX, TAPE, MD: 90 dB (SOURCE DIRECT: ON)
Frequency Response: 5 Hz to 80 kHz: +0.5, -3 dB (SOURCE DIRECT: ON)
Tone Control Adjustable Range: BASS: 100 Hz \pm 8 dB
 TREBLE: 10 kHz \pm 8 dB
 LOUDNESS: 100 Hz +8 dB
 10 kHz +6 dB

• TUNER SECTION

Reception frequency range: FM: 87.50 MHz~108.00 MHz
 AM: 522 kHz~1611 kHz
Practical sensitivity: FM: 1.2 μ V/75 Ω /ohms (12.8 dBf)
 AM: 18 μ V
FM stereo isolation: 40 dB (1 kHz)
FM S/N ratio: Monaural: 74 dB Stereo: 70 dB
FM harmonic distortion: Monaural: 0.3% Stereo: 0.4%

• CLOCK/TIMER SECTION

Clock system: Power source frequency synchronization system
Clock precision (per month): \pm 30 seconds
Timer functions: Everyday timer (1 setting)
 Once timer (1 setting)
 Sleep timer (maximam 60 min.)

• OTHERS

Power supply: AC 230 V, 50 Hz
Power consumption: 85 W
 (Approx. 1 W in standby mode)
Dimensions: 250 (W) \times 81.5 (H) \times 285 (D) mm
Net Mass: 3.5 kg

• REMOTE CONTROL UNIT (RC-927)

Remote control system: Infrared pulse system
Power supply: 3 V DC, Two size R03 ("AAA")
 dry cell batteries
External dimensions: 48 (W) \times 210 (H) \times 29 (D) mm
Mass: 120 g (including batteries)

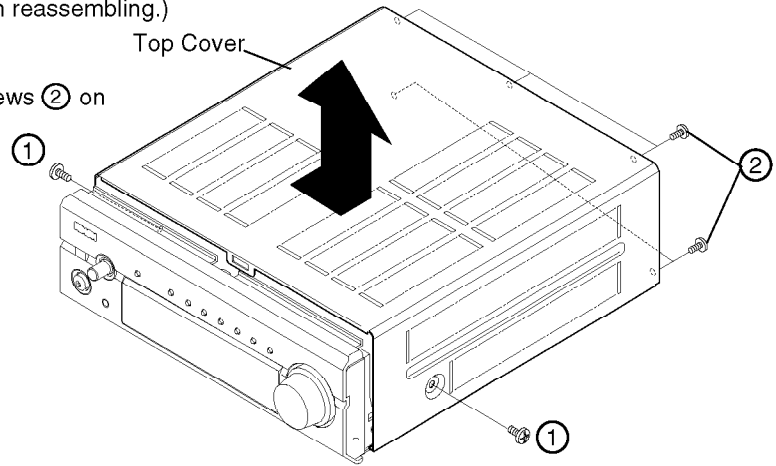
* For improvement purposes, specifications and functions are subject to change without advanced notice.

DISASSEMBLY

(Follow the procedure below in reverse order when reassembling.)

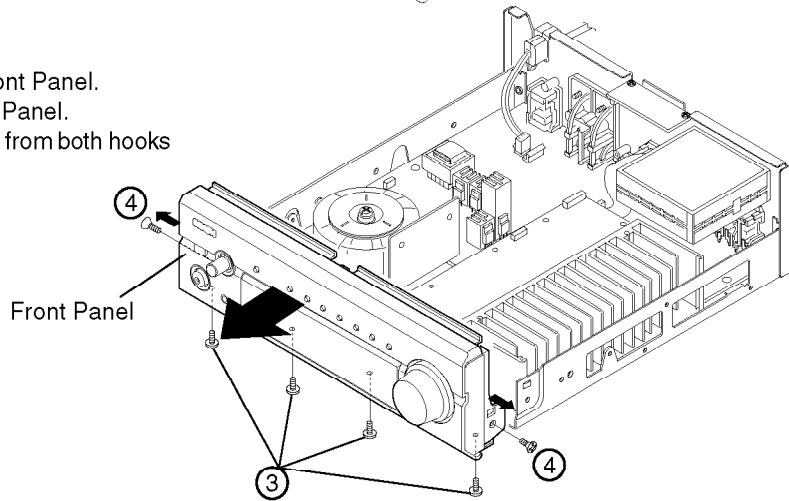
1. Top Cover

Remove 2 screws ① on both sides, and 5 screws ② on the rear.



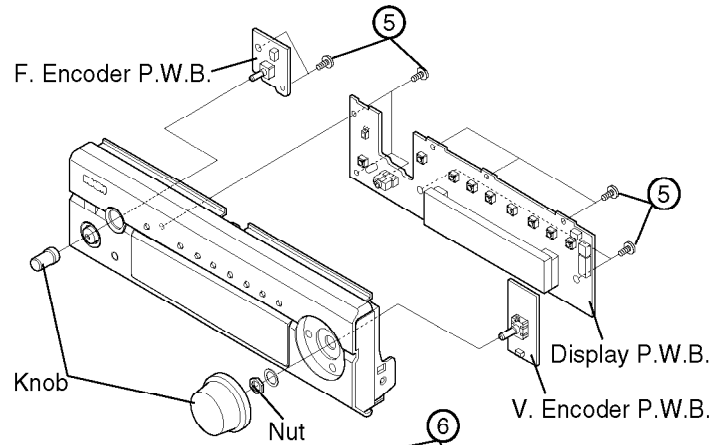
2. Front Panel

- (1) Remove 4 bottom screws ③ from the Front Panel.
- (2) Remove 2 side screws ④ from the Inner Panel.
- (3) Widen the Inner Panel as shown to release from both hooks of the Chassis.



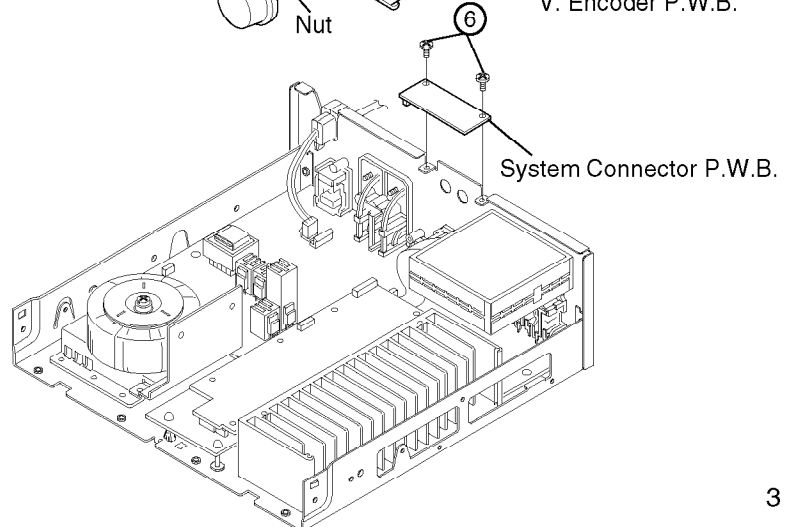
3. Display P.W.B., Encoder P.W.B.

- (1) Remove 2 knobs and 1 nut.
- (2) Remove 10 screws ⑤.



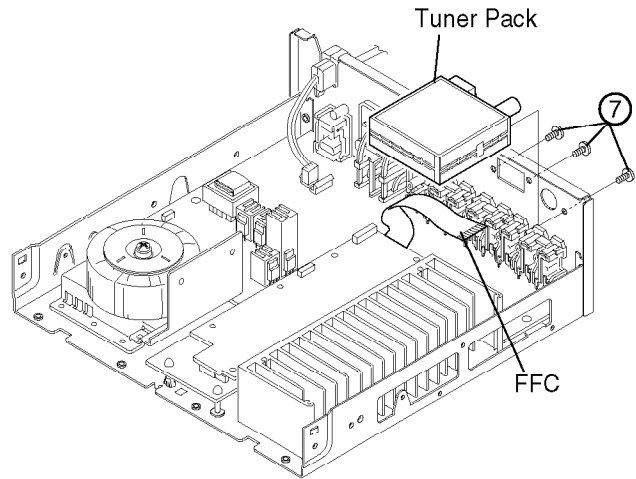
4. System Connector P.W.B.

Remove 2 screws ⑥.



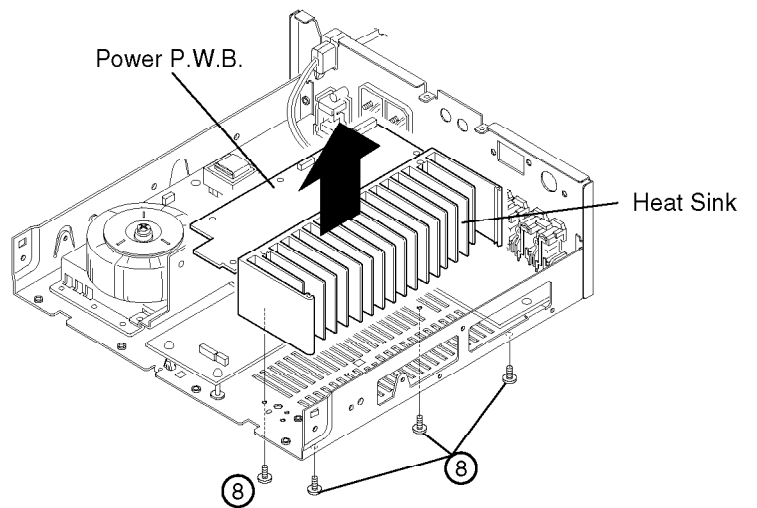
5. Tuner Pack

- (1) Disconnect the FFC.
- (2) Remove 3 screws ⑦.



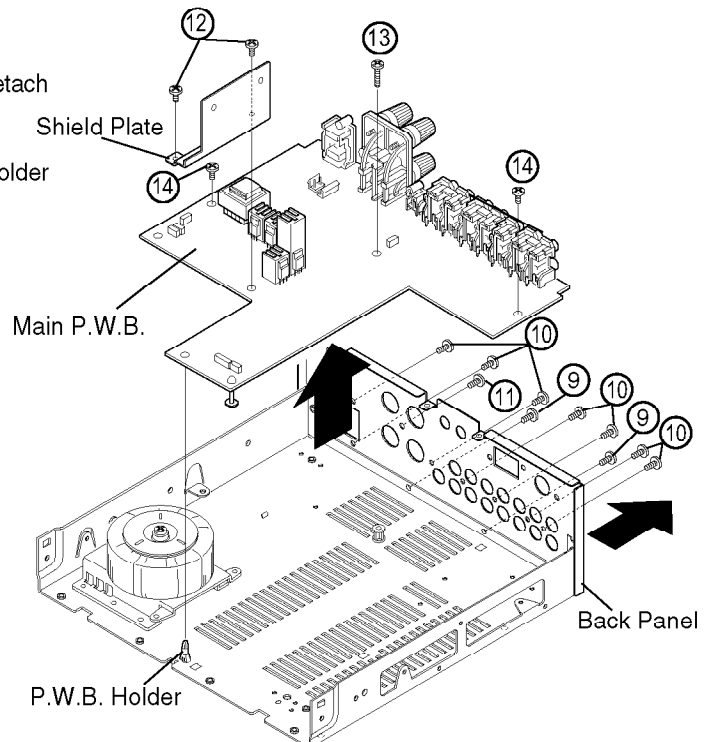
6. Power P.W.B., Heat Sink

- (1) Remove 4 screws ⑧.
- (2) Detach the Power P.W.B. and Heat Sink to the arrow direction.

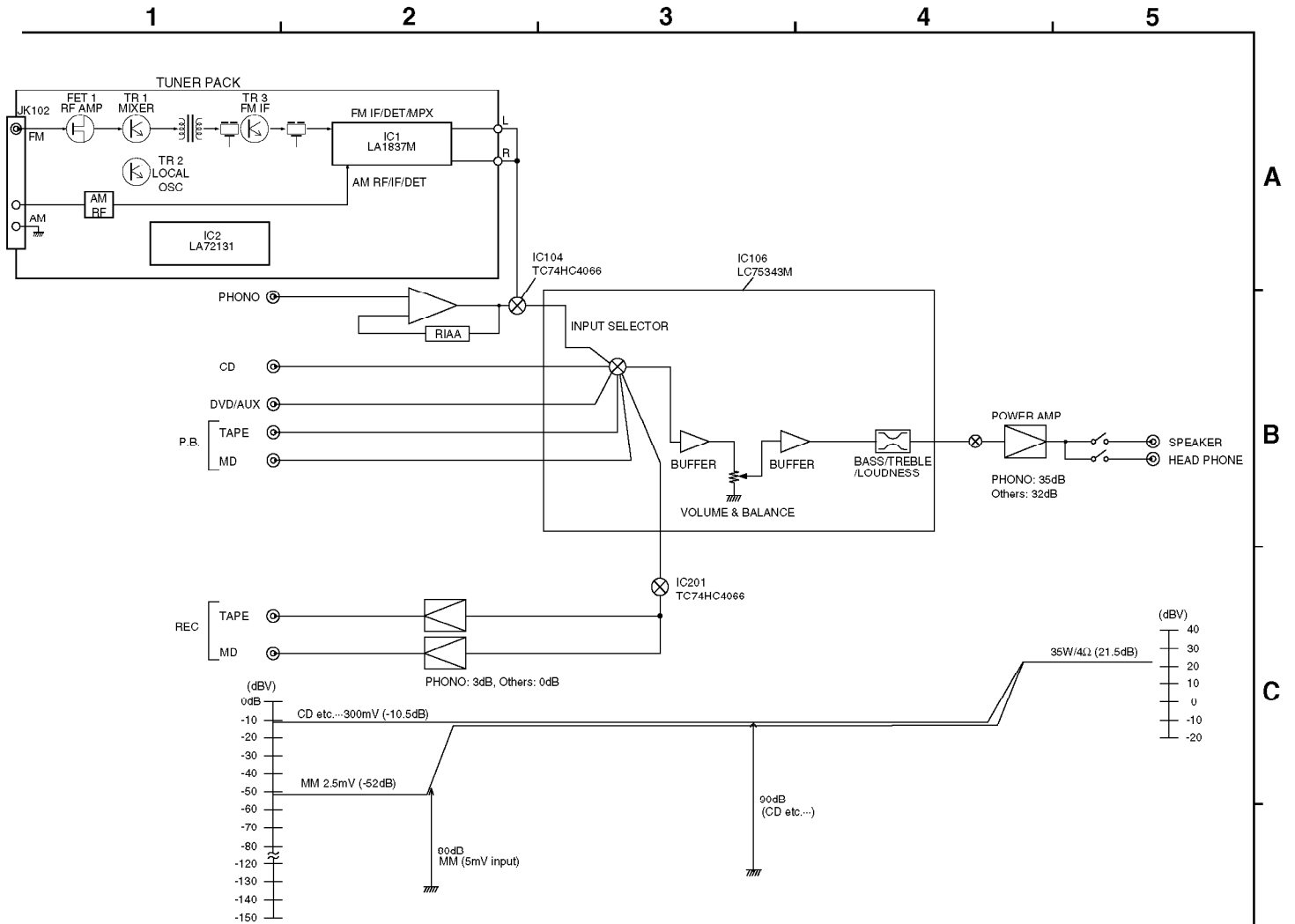


7. Shield Plate, Back Panel, Main P.W.B.

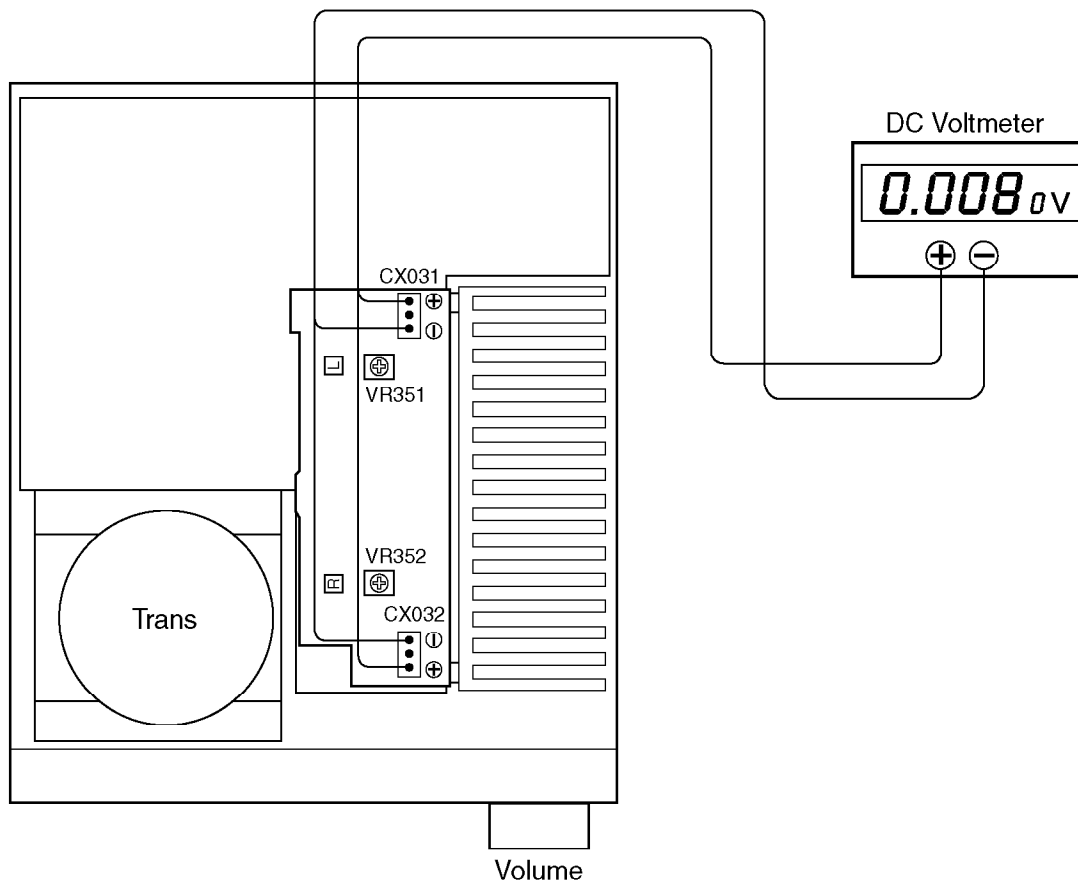
- (1) Remove 2 screws ⑨, 7 screws ⑩, and 1 screw ⑪ to detach the Back Panel.
- (2) Remove 2 screws ⑫ to detach the Shield Plate.
- (3) Remove 1 screw ⑬, 2 screws ⑭ and release P.W.B. holder to detach the Main P.W.B.



BLOCK AND LEVEL DIAGRAM



ADJUSTMENT



IDLING CURRENT

● Setup

1. Place the unit at an ordinary position avoiding direct air flow from an air-conditioner or fan. Do the adjustment at a temperature between 15 °C (59 °F) and 30 °C (86 °F).
2. Set control as follows.

VOLUME control	→	fully counterclockwise (⤴ min.)
SPEAKER terminals	→	open: do not connect the speakers, dummy load etc.

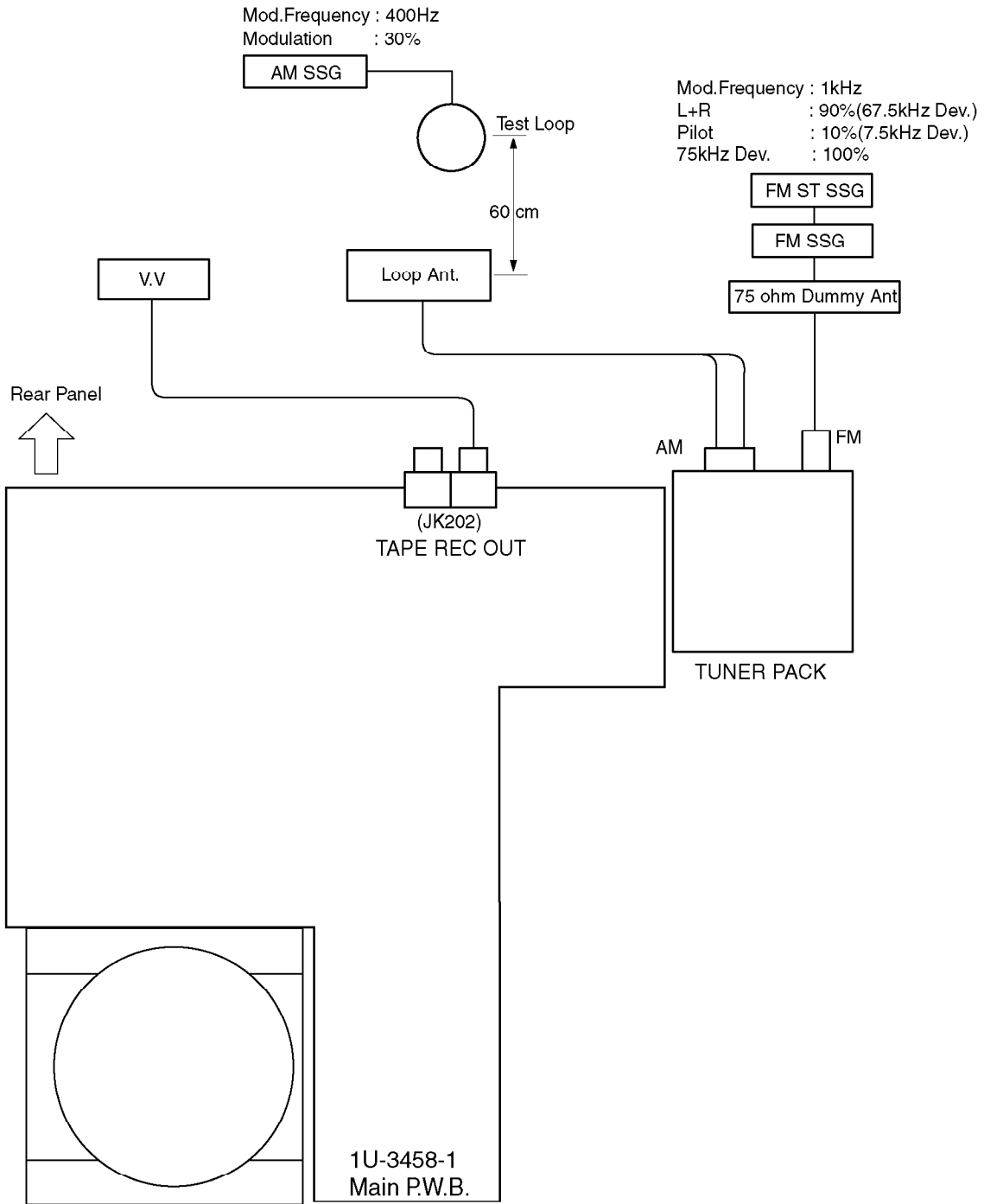
● Adjustment

1. Remove top cover. And then connect DC voltmeter to the test points CX031 and CX032 of POWER P.W.B.
2. Connect power cord to AC230V (218 ~ 242V) wall outlet, and turn POWER switch "ON".
3. Right after power on, adjust VR351 and VR352 so that the DC voltmeter reads $10 \pm 1\text{mV}$.
4. Then after 2 minutes warmup adjust VR351 and VR352 so that the DC voltmeter reads $10 \pm 1\text{mV}$.
5. And after 10 minutes warmup adjust VR351 and VR352 so that the DC voltmeter reads $10 \pm 0.5\text{mV}$.

CHECK

● Connection Diagram

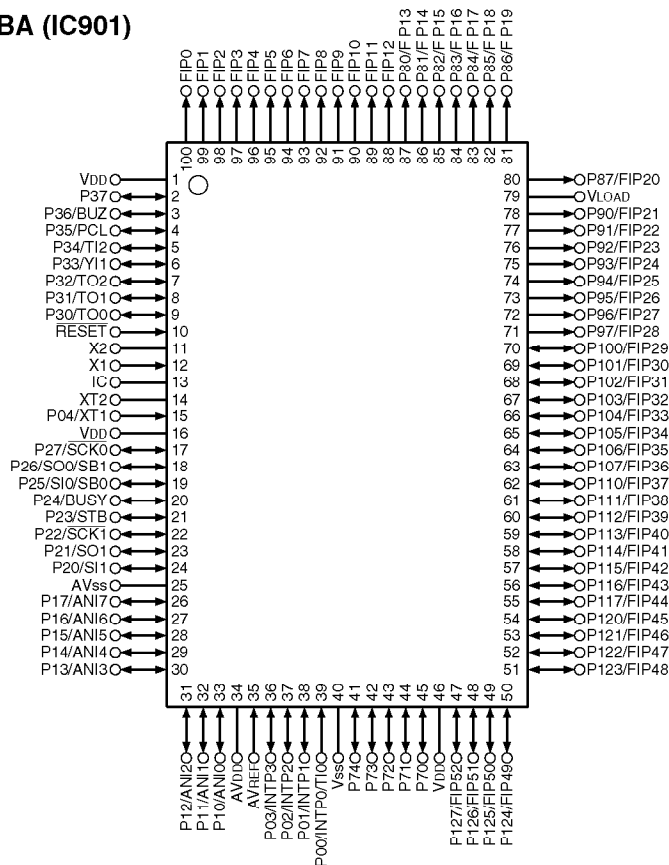
1U-3458-1 Main P.W.B. (Component Side)



SEMICONDUCTORS

● IC's

μPD780208GF-142-3BA (IC901)



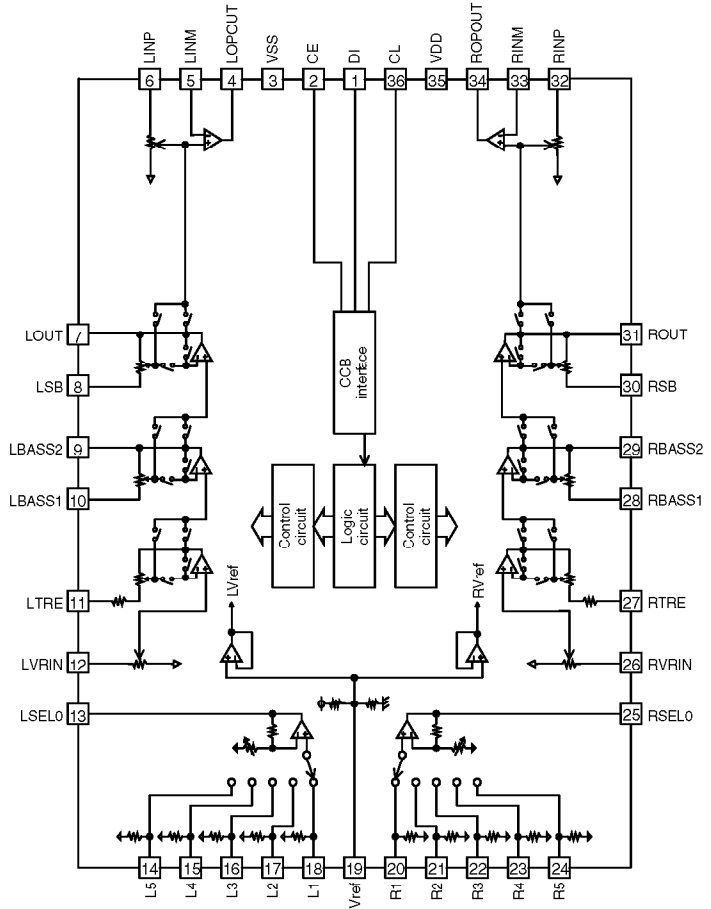
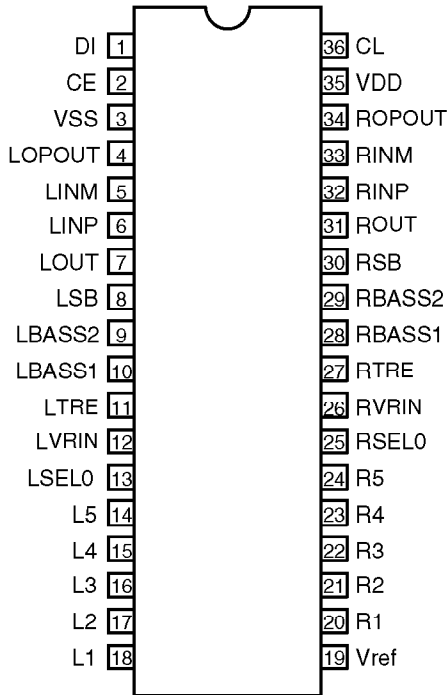
μPD780208GF-142-3BA Terminal Function

Pin No.	Pin Name	Symbol	I/O	Det	Res	Ext	Act	INIT	MO	Function
1	VDD	+5V	-	-	-	-	-	-	-	Positive power
2	P37	RL HP	O	Lv	Z	-	H	-	-	Headphone relay output
3	P36/BUZ	LED-G	O	-	Z	-	H	L	-	Power on LED drive output
4	P35/PCL	LED-R	O	-	Z	-	H	L	-	Standby LED drive output
5	P34/TI2	_MD OUT	O	-	Z	Pu	L	L	-	Rec out MD switching SW, L: REC OUT ON
6	P33/TI1	_TAPE OUT	O	-	Z	Pu	L	L	-	Rec out TAPE switching SW, L: REC OUT ON
7	P32/TO2	NC	O	-	Z	Pu	L	L	-	Not used
8	P31/TO1	NC	O	-	Z	Pu	L	L	-	Not used
9	P30/TO0	TU/_PHONO	O	-	Z	-	L	L	-	Tuner/Phono switching output, L: PHONO, H: OTHERS
10	RESET	RESET	I	-	-	-	L	-	-	μcom reset input
11	X2	X2	-	-	-	-	-	-	-	X'tal connection for main clock oscillation
12	X1	X1	I	-	-	-	-	-	-	X'tal connection for main clock oscillation 4.19MHz
13	IC	IC	-	-	-	-	-	-	-	Internal connection, connect directly to Vss
14	XT2	XT2	-	-	-	-	-	-	-	X'tal connection for sub-clock oscillation, not used: NC
15	P04/XT1	P04/XT1	I	-	Z	-	-	-	-	X'tal connection for sub-clock oscillation, not used: connect to Vss or VDD
16	VDD	VDD	-	-	-	-	-	-	-	Positive power
17	P27/SCK0	DB_CLK	O	-	Z	-	-	H	-	DENON bus clock output
18	P26/SO0/SB1	DB_TXD	O	-	Z	-	-	H	-	DENON bus data output
19	P25/SI0/SB0	DB_RXD	I	Ed	Z	-	-	-	-	DENON bus data input
20	P24/BUSY	RDSRST	O	-	Z	-	L	H	-	RDS IC reset output
21	P23/STB	DATA_CE	O	-	Z	-	H	L	-	Data bus (PLL, VOL, RDS IC common) chip enable output
22	P22/SCK1	DATA_CLK	O	-	Z	-	-	H	-	Data bus (PLL, VOL, RDS IC common) clock output
23	P21/SO1	DATA_TXD	O	-	Z	-	-	H	-	Data bus (PLL, VOL, RDS IC common) data output
24	P20/SI1	DATA_RXD	I	Ed	Z	-	-	-	-	Data bus (PLL, VOL, RDS IC common) data input
25	AVSS	AVSS	-	-	-	-	-	-	-	Ground potential of A/D converter
26	P17/ANI7	NC	O	-	Z	Pu	-	L	-	Not used
27	P16/ANI6	NC	O	-	Z	Pu	-	L	-	Not used
28	P15/ANI5	V-STB	O	-	Z	-	H	L	-	LC75343M IC strobe output
29	P14/ANI4	NC	O	-	Z	Pu	-	L	-	Not used
30	P13/ANI3	CE-R	O	-	Z	-	H	L	-	EEPROM chip enable output
31	P12/ANI2	PWB	I	-	Z	Pu	-	-	-	For PWB check mode activation
32	P11/ANI1	KEY2	I	A	Z	Pu	-	-	-	Main unit operation button input 2

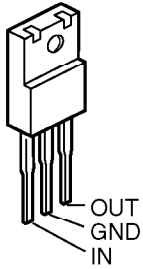
Pin No.	Pin Name	Symbol	I/O	Det	Res	Ext	Act	INIT	MO	Function
33	P10/ANI0	KEY1	I	A	Z	Pu	-	-	-	Main unit operation button input 1
34	AVDD	AVDD	-	-	-	-	-	-	-	Analog power for A/D converter
35	AVREF	AVREF	I	-	-	-	-	-	-	Ref. V input for A/D converter
36	P03/INTP3	PROTECT	I	-	Z	-	L	-	-	Amp protect input (µcom inside pull-up)
37	P02/INTP2	DBRXD	I	Ed	Z	-	-	-	-	DENON bus data input (µcom inside pull-up)
38	P01/INTP1	50/60	I	Ed	Z	-	-	-	-	50/60Hz AC input (µcom inside pull-up)
39	P00/INTP0	REMOCON	I	-	Z	-	H	-	-	Remote control input (µcom inside pull-up)
40	VSS	VSS	-	-	-	-	-	-	-	GND
41	P74	RL-SPA	O	-	Z	Pu	L	L	-	Not used
42	P73	RL-SPB	O	-	Z	Pu	L	H	-	Speaker relay output, L: OUTPUT
43	P72	CLOCK	O	-	Z	Pu	H	L	-	Clock output for LC75343M IC
44	P71	DATA	O	-	Z	Pu	-	L	-	Data output for LC75343M IC
45	P70	TMUTE	O	-	Z	Pu	L	L	N	Tuner mute output, L: MUTE ON
46	VDD	VDD	-	-	-	-	-	-	-	Positive power
47	P127/FIP52	ENC-A	I	-	Z	Pu	-	-	-	Rotary encoder input A for volume
48	P126/FIP51	ENC-B	I	-	Z	Pu	-	-	-	Rotary encoder input B for volume
49	P125/FIP50	SEL-A	I	-	Z	Pu	-	-	-	Rotary encoder input A for select
50	P124/FIP49	SEL-B	I	-	Z	Pu	-	-	-	Rotary encoder input B for select
51	P123/FIP48	NC	O	-	Z	Pd	-	L	-	Not used
52	P122/FIP47	AM STEREO	I/O	Lv	Z	Pd	L	-	N	AM stereo demodulate detect input, L: STEREO
53	P121/FIP46	SD	I	Lv	Z	Pu	L	-	N	AM/FM tuned signal input, L: TUNED
54	P120/FIP45	ST IND	I	Lv	Z	Pu	L	-	N	FM stereo demodulate detect input, L: STEREO
55	P117/FIP44	USA	I	Lv	Z	-	-	-	N	Initial setting input
56	P116/FIP43	EUR	I	Lv	Z	-	-	-	N	Initial setting input
57	P115/FIP42	FREQ	I	Lv	Z	-	-	-	N	Initial setting input
58	P114/FIP41	RDS	I	Lv	Z	-	-	-	N	Initial setting input
59	P113/FIP40	VMUTE	O	-	Z	Pu	H	L	-	Mute output at volume 00, H: MUTE
60	P112/FIP39	POWER	O	-	Z	Pu	L	L	N	Amp circuit power on/off output, L: ON
61	P111/FIP38	NC	O	-	Z	Pd	-	L	N	Not used
62	P110/FIP37	SP OFF	I	-	Z	Pd	L	L	N	Headphone input
63	P107/FIP36	NC	O	-	Z	Pd	-	L	-	Not used
64	P106/FIP35	P1	O	-	Z	Pd	-	-	N	Segment output 1
65	P105/FIP34	P2	O	-	Z	Pd	-	-	N	Segment output 2
66	P104/FIP33	P3	O	-	Z	Pd	-	-	N	Segment output 3
67	P103/FIP32	P4	O	-	Z	Pd	-	-	N	Segment output 4
68	P102/FIP31	P5	O	-	Z	Pd	-	-	N	Segment output 5
69	P101/FIP30	P6	O	-	Z	Pd	-	-	N	Segment output 6
70	P100/FIP29	P7	O	-	Z	Pd	-	-	N	Segment output 7
71	P97/FIP28	P8	O	-	Z	Pd	-	-	N	Segment output 8
72	P96/FIP27	P9	O	-	Z	Pd	-	-	N	Segment output 9
73	P95/FIP26	P10	O	-	Z	Pd	-	-	N	Segment output 10
74	P94/FIP25	P11	O	-	Z	Pd	-	-	N	Segment output 11
75	P93/FIP24	P12	O	-	Z	Pd	-	-	N	Segment output 12
76	P92/FIP23	P13	O	-	Z	Pd	-	-	N	Segment output 13
77	P91/FIP22	P14	O	-	Z	Pd	-	-	N	Segment output 14
78	P90/FIP21	P15	O	-	Z	Pd	-	-	N	Segment output 15
79	VLOAD	VLOAD	-	-	-	-	-	-	-	FL driver pull-down resistor connection (-30V power)
80	P87/FIP20	P16	O	-	Z	Pd	-	-	N	Segment output 16
81	P86/FIP19	P17	O	-	Z	Pd	-	-	N	Segment output 17
82	P85/FIP18	P18	O	-	Z	Pd	-	-	N	Segment output 18
83	P84/FIP17	P19	O	-	Z	Pd	-	-	N	Segment output 19
84	P83/FIP16	P20	O	-	Z	Pd	-	-	N	Segment output 20
85	P82/FIP15	P21	O	-	Z	Pd	-	-	N	Segment output 21
86	P81/FIP14	P22	O	-	Z	Pd	-	-	N	Segment output 22
87	P80/FIP13	P23	O	-	Z	Pd	-	-	N	Segment output 23
88	FIP12	NC	O	-	Z	-	-	-	-	Segment output 24 (not connected)
89	FIP11	NC	O	-	Z	-	-	-	-	Segment output 25 (not connected)
90	FIP10	11G	O	-	Z	Pd	-	-	-	Digit output 11
91	FIP9	10G	O	-	Z	Pd	-	-	-	Digit output 10
92	FIP8	9G	O	-	Z	Pd	-	-	-	Digit output 9
93	FIP7	8G	O	-	Z	Pd	-	-	-	Digit output 8
94	FIP6	7G	O	-	Z	Pd	-	-	-	Digit output 7
95	FIP5	6G	O	-	Z	Pd	-	-	-	Digit output 6
96	FIP4	5G	O	-	Z	Pd	-	-	-	Digit output 5
97	FIP3	4G	O	-	Z	Pd	-	-	-	Digit output 4
98	FIP2	3G	O	-	Z	Pd	-	-	-	Digit output 3
99	FIP1	2G	O	-	Z	Pd	-	-	-	Digit output 2
100	FIP0	1G	O	-	Z	Pd	-	-	-	Digit output 1

Det (Detecting method) : Lv (Level detect), Ed (Edge detect), L/E (Level/Edge detect), A (Analog voltage detect)
 Res (Status at reset) : Z (Hi-impedance at reset)
 Ext (Pull-down/Pull-up) : Pd (Pull-down), Pu (Pull-up)
 MO (Mask option) : Y (Yes), N (No)

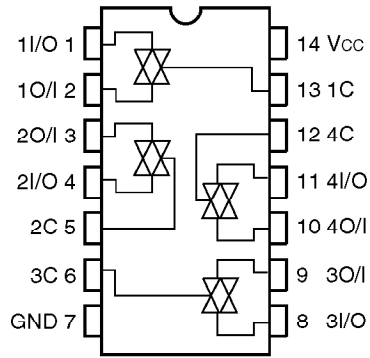
LC75343M (IC106)



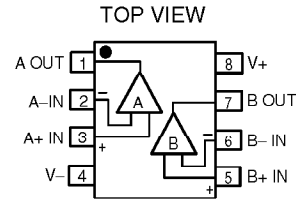
**BA05T (IC907)
NJM7812FA(S) (IC101)**



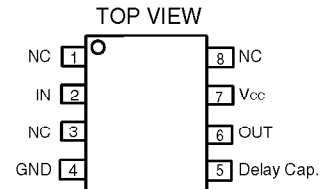
TC74HC4066AF (IC104, 201)



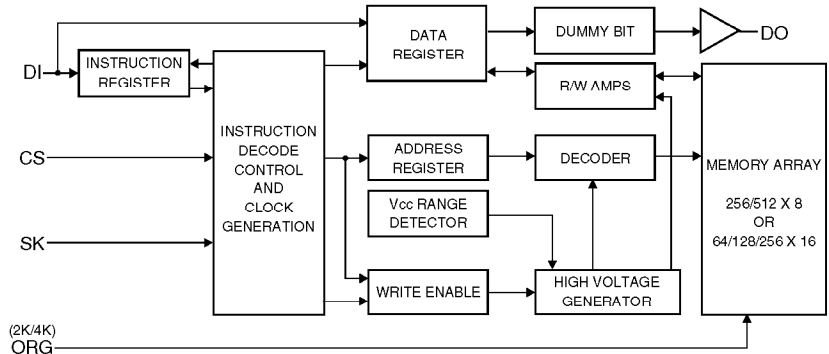
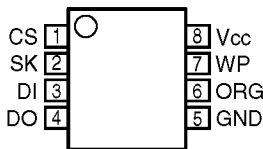
**BA15218F (IC202, 203)
NJM2068DDC (IC401)
NJM5532DD (IC309)**



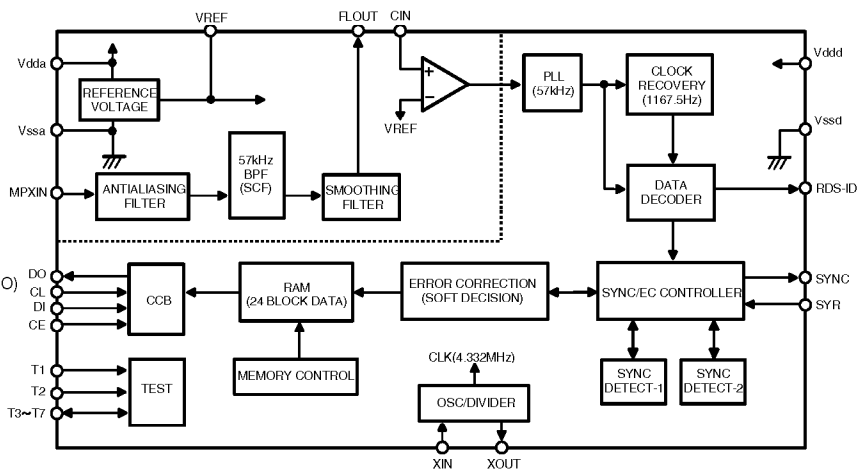
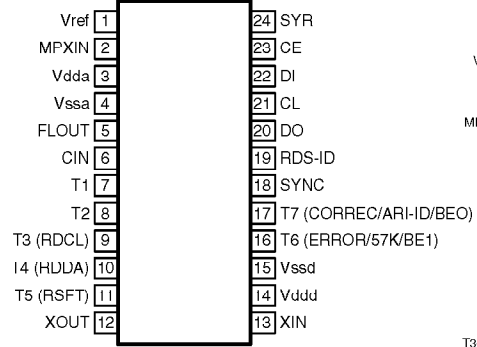
M51957BFP (IC903)



93LC66 (IC905)

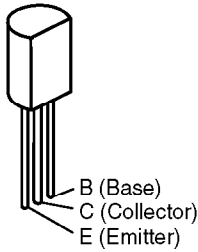


LC72720NM (IC601)

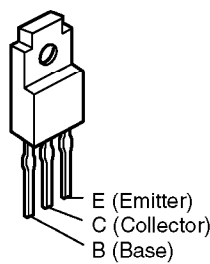


● TRANSISTORS

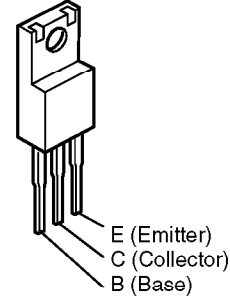
2SA988 (E/F)
2SC1841 (E/F)



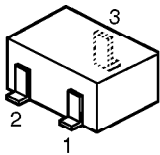
2SA1837 (Y)
2SC4793 (Y)



2SC4495 (O/Y)
2SD1913

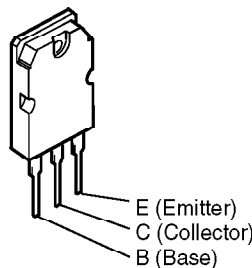


2SC2412K (S)
2SA1037K (S/R)

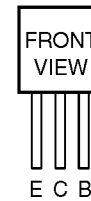


1: E (Emitter)
2: B (Base)
3: C (Collector)

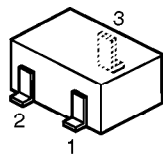
2SB1383 (P/Y)
2SD2083 (P/Y)



2SB1328

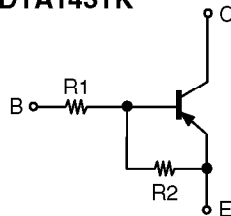


DTA114EK
DTA114YK
DTA115TK
DTA143TK
DTC114EK
DTC124EK
DTC143TK
DTC144EK
DTC323TK



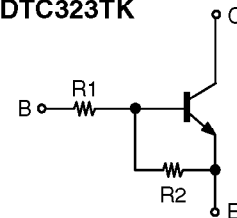
1: E (Emitter)
2: B (Base)
3: C (Collector)

DTA114EK
DTA114YK
DTA115TK
DTA143TK



	R1	R2
DTA114EK	10 kohm/Ω	10 kohm/Ω
DTA114YK	10 kohm/Ω	47 kohm/Ω
DTA115TK	100 kohm/Ω	—
DTA143TK	4.7 kohm/Ω	—

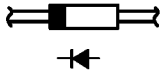
DTC114EK
DTC124EK
DTC143TK
DTC144EK
DTC323TK



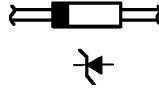
	R1	R2
DTC114EK	10 kohm/Ω	10 kohm/Ω
DTC124EK	22 kohm/Ω	22 kohm/Ω
DTC143TK	4.7 kohm/Ω	—
DTC144EK	47 kohm/Ω	47 kohm/Ω
DTC323TK	2.2 kohm/Ω	—

● DIODES (including LED)

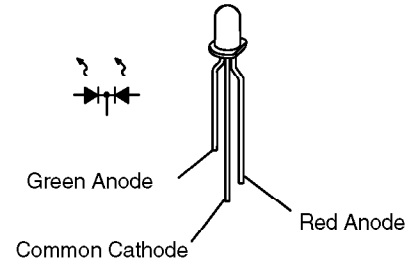
1N4004
1N4148



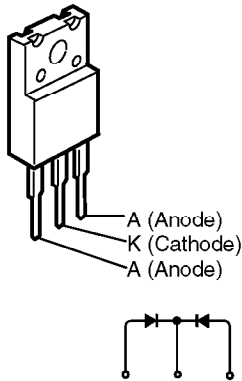
MTZJ4.3B
MTZJ6.2B
MTZJ10B
MTZJ18B
MTZJ33B



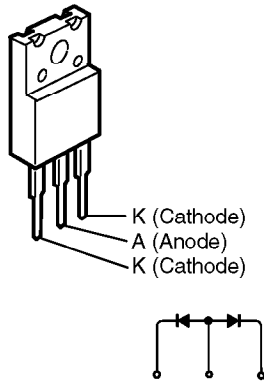
SLR-9336DS-91 (LD901)



FMG22S

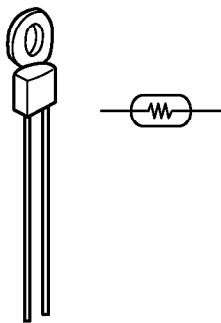


FMG22R



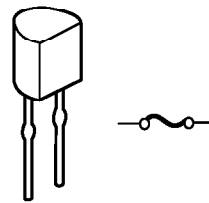
● POSISTOR

PTH9M04BC222TS2F333 (PH701)



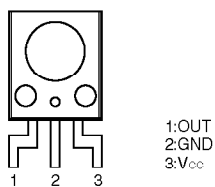
● IC PROTECTOR

ICP-N15 (IC107, 108, 906, 999)



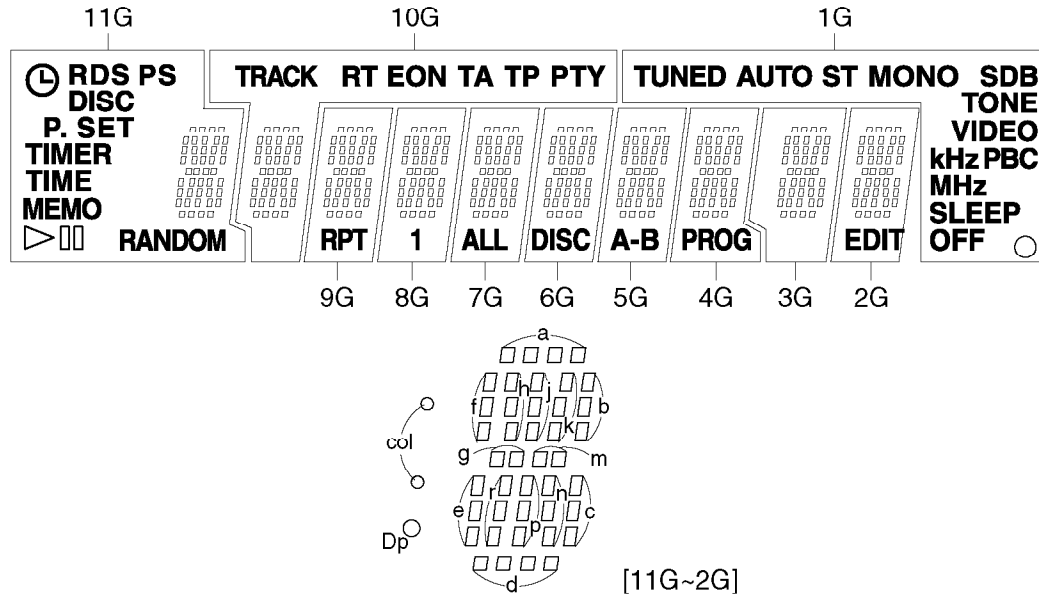
● IR SENSOR

RPM6938-V4 (IC902)



● FL DISPLAY 11-BT-199GNK (FL901)

GRID ASSIGNMENT



PIN CONNECTION

PIN NO.	1	2	3	4	5	6	7	8	9	0	1	1	1	1	1	1	1	1	1	1	2	2	2	2	2	2	2	3	3	3	3	3	3	3	4	4	4	4	4	4	4		
CONNECTION	F	N	N	N	1	2	3	4	5	6	7	8	9	0	1	N	2	2	2	2	2	1	1	1	1	1	1	1	1	P	P	P	P	P	P	P	N	N	N	F			
	1	X	P	P	G	G	G	G	G	G	G	G	G	C	5	4	3	2	1	0	9	8	7	6	5	4	3	2	1	0	9	8	7	6	5	4	3	2	1	P	P	X	2

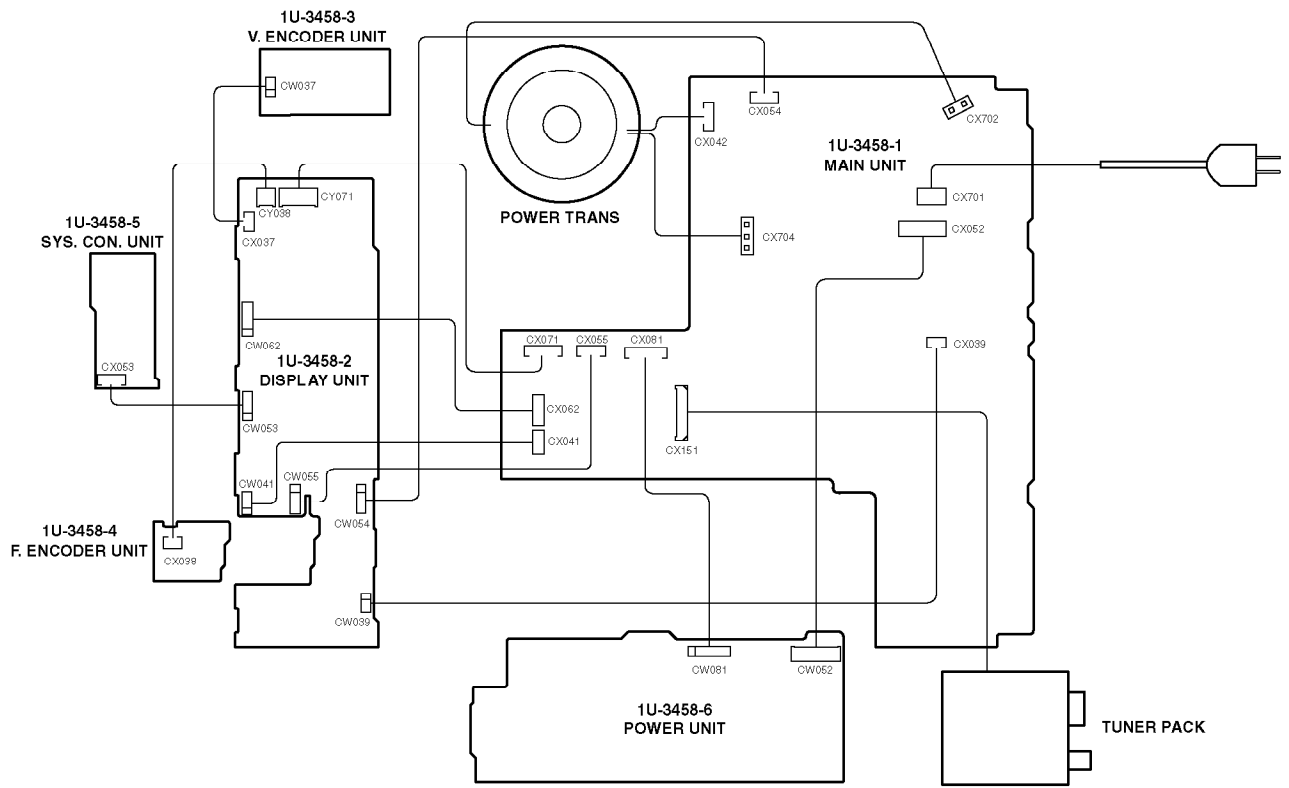
- NOTE
- 1) F1,F2 ---- Filament
 - 2) NP ----- No pin
 - 3) NX ----- No extend pin
 - 4) NC ----- No connection
(NC pin should be electrically open on the PC board)
 - 5) DL ----- Datum Line
 - 6) 1G~11G -- Grid

ANODE CONNECTION

	11G	10G	9G	8G	7G	6G	5G	4G	3G	2G	1G
P1	a	a	a	a	a	a	a	a	a	a	TUNED
P2	b	b	b	b	b	b	b	b	b	b	AUTO
P3	c	c	c	c	c	c	c	c	c	c	ST
P4	d	d	d	d	d	d	d	d	d	d	MONO
P5	e	e	e	e	e	e	e	e	e	e	SDB
P6	f	f	f	f	f	f	f	f	f	f	TONE
P7	g	g	g	g	g	g	g	g	g	g	PBC
P8	h	h	h	h	h	h	h	h	h	h	—
P9	j	j	j	j	j	j	j	j	j	j	VIDEO
P10	k	k	k	k	k	k	k	k	k	k	kHz
P11	m	m	m	m	m	m	m	m	m	m	MHz
P12	n	n	n	n	n	n	n	n	n	n	—
P13	p	p	p	p	p	p	p	p	p	p	—
P14	r	r	r	r	r	r	r	r	r	r	SLEEP
P15	RANDOM	TRACK	RPT	1	ALL	DISC	A-	PROG	col	EDIT	OFF
P16	Ⓞ	RT	—	—	—	—	B	—	Dp	—	Ⓞ
P17	RDS	EON	—	—	—	—	—	—	—	—	—
P18	PS	TA	—	—	—	—	—	—	—	—	—
P19	DISC	TP	—	—	—	—	—	—	—	—	—
P20	P. SET	PTY	—	—	—	—	—	—	—	—	—
P21	TIMER	—	—	—	—	—	—	—	—	—	—
P22	TIME	—	—	—	—	—	—	—	—	—	—
P23	MEMO	—	—	—	—	—	—	—	—	—	—
P24	▶	—	—	—	—	—	—	—	—	—	—
P25		—	—	—	—	—	—	—	—	—	—

WIRING DIAGRAM

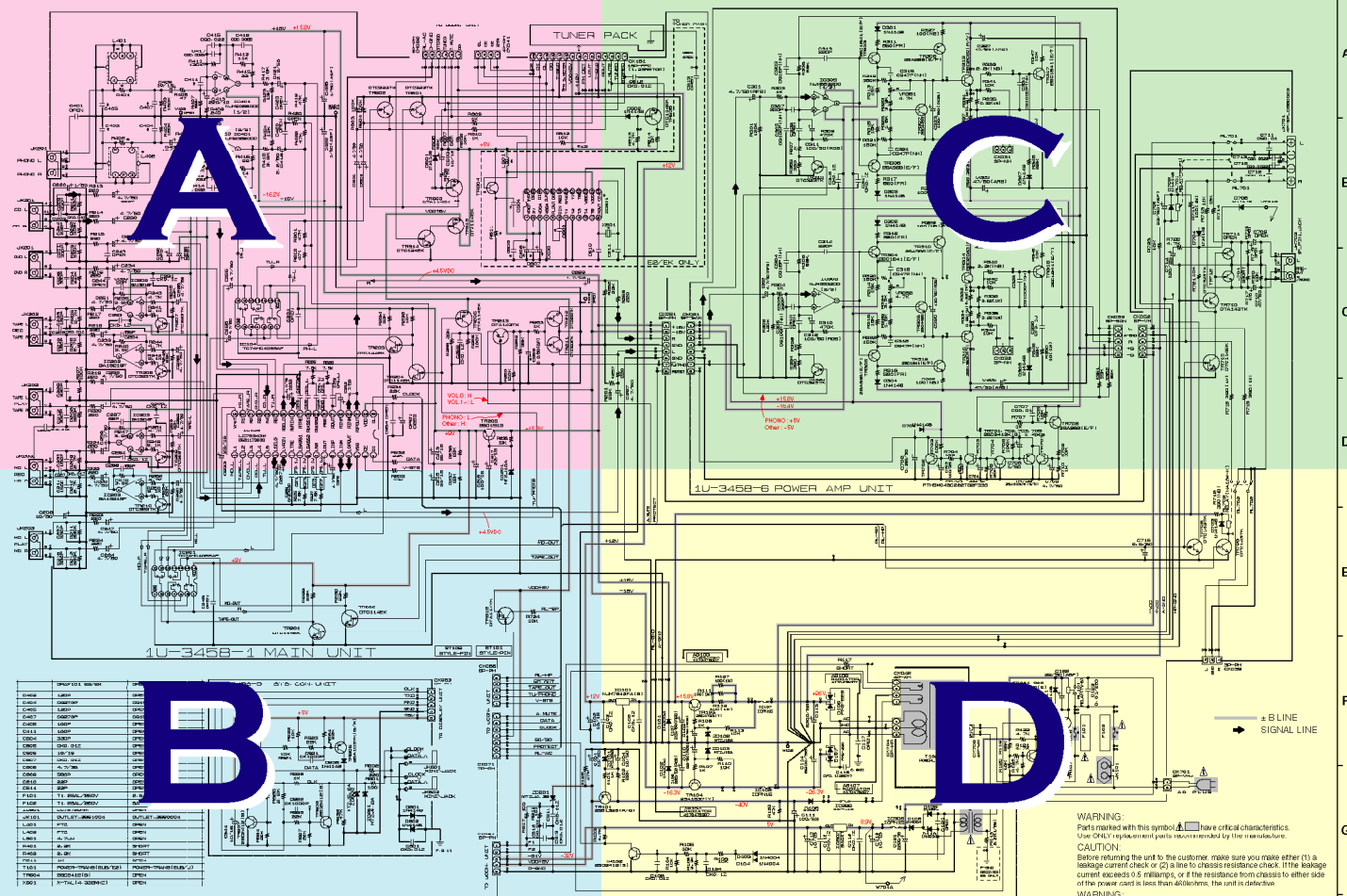
1 2 3 4 5 6 7 8



A
B
C
D
E

SCHEMATIC DIAGRAMS (1/2)

1 2 3 4 5 6 7 8 9 10 11



SCHEMATIC DIAGRAMS (1/2)
1U-345B-4 MAIN UNIT
1U-345B-5 SYS. CONN. UNIT
1U-345B-6 POWER AMP UNIT

SCHEMATIC DIAGRAMS (1/2)

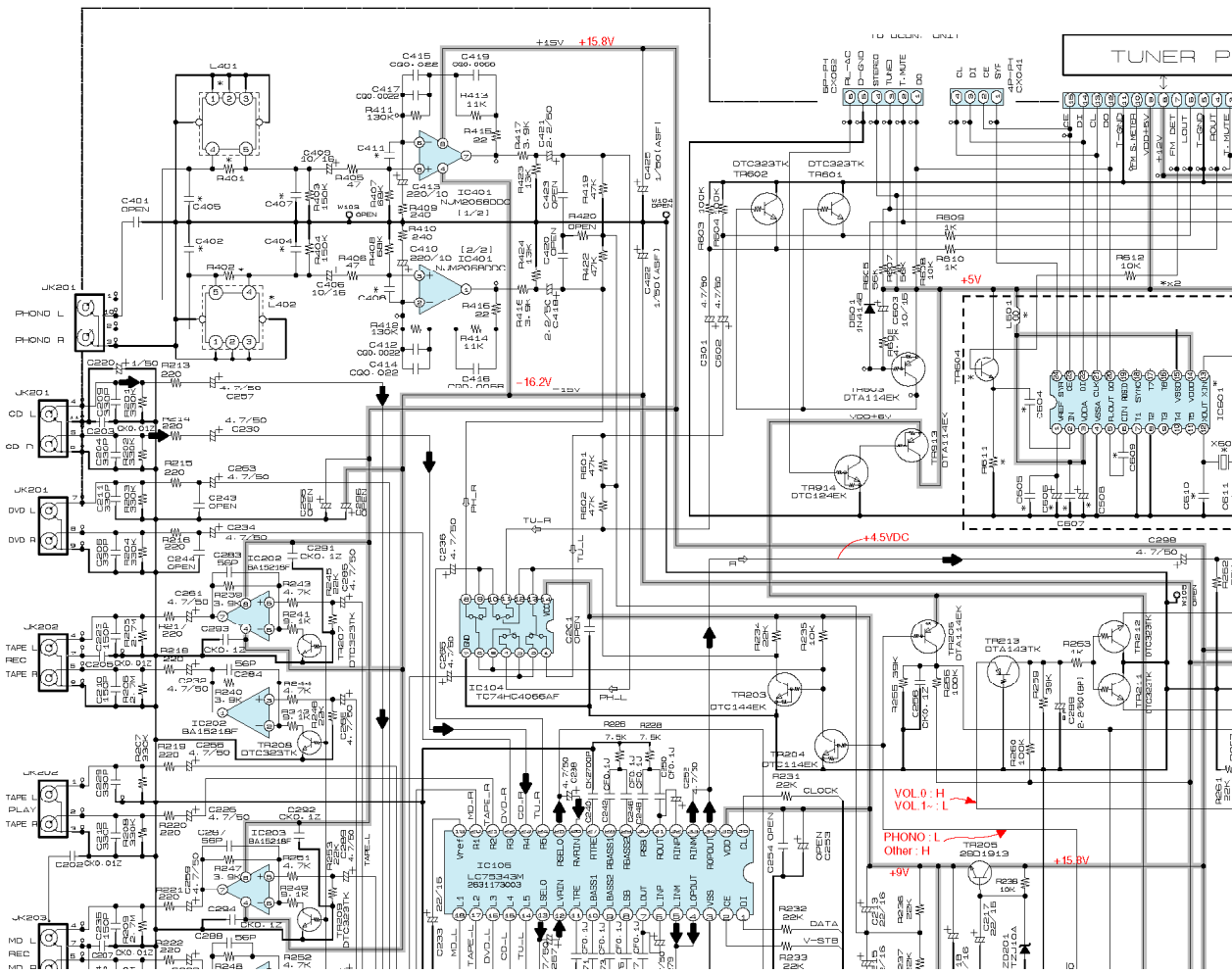
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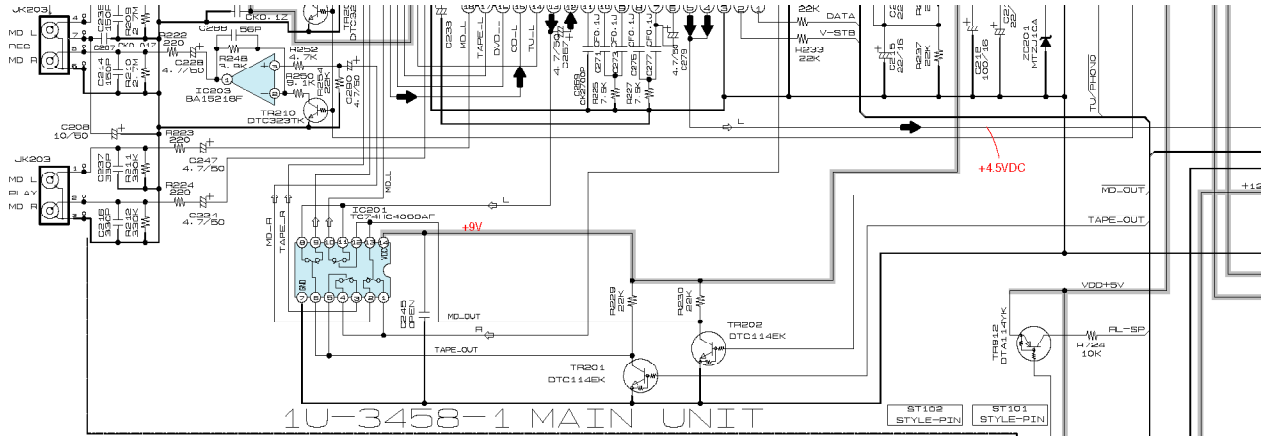
2

3

4

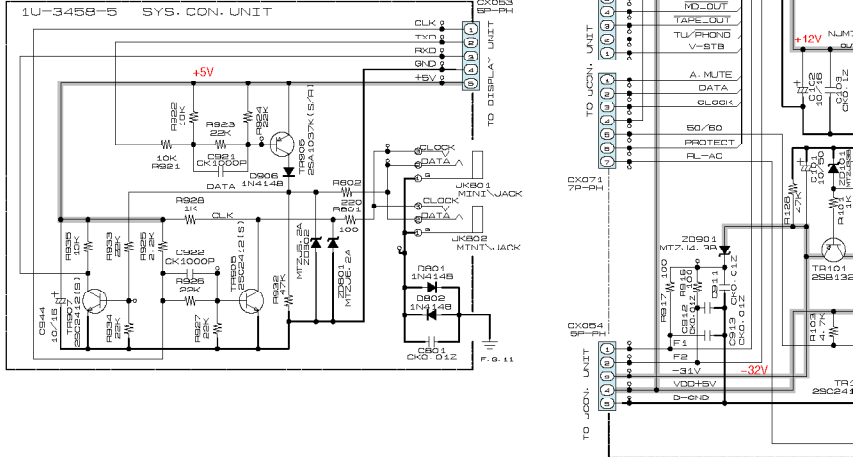
5





1U-3458-1 MAIN UNIT

	DRAF101 E2/EK	DRAF101 J
F405	120P	OPEN
C404	C6270P	CO150P
C405	120P	OPEN
C407	C6270P	GB150P
C408	100P	OPEN
C411	100P	OPEN
U504	33UJ	U-EN
C605	CKO.012	OPEN
C606	10/16	OPEN
C607	CKO.012	OPEN
C608	4.7/30	OPEN
C609	560P	OPEN
C610	22P	OPEN
U611	62P	U-EN
F101	T1.25A/250V	2.5A
F105	T1.25A/250V	5A
IC601	LC72720NH	OPEN
XK304	OUTLET 3061004	OUTLET 3000004
L401	FTZ	OPEN
L602	FTZ	OPEN
L601	4.7UH	OPEN
R401	2.2K	SHORT
R402	2.2K	SHORT
R611	1K	OPEN
T101	DWEP-T0ANR(SI/S/E2)	DWEP-T0ANR(SI/S/1)
TR604	2SC841B(S)	OPEN
X801	X-TAL(4.332M42)	OPEN



1U-3458-5 SYS. CON. UNIT

6

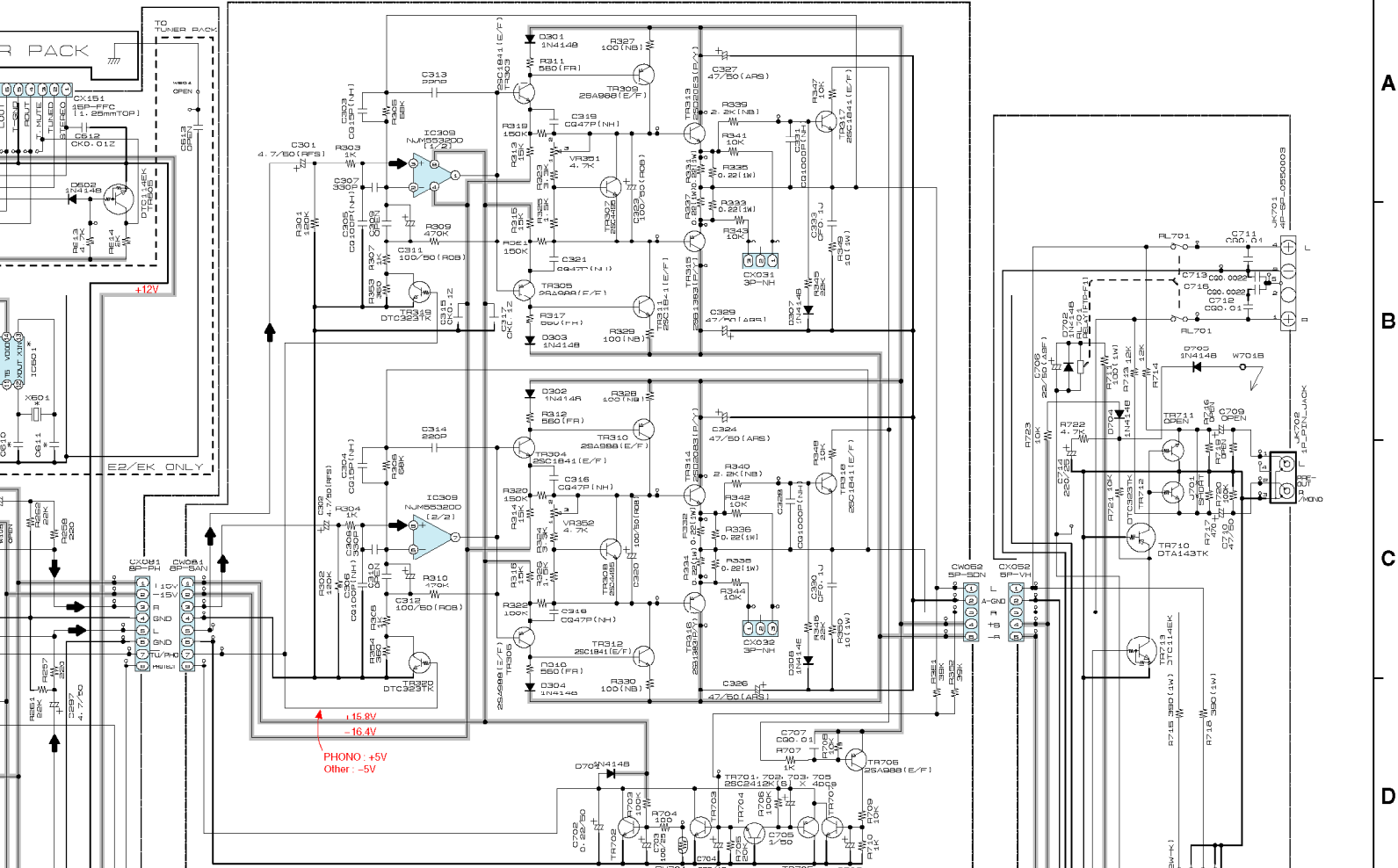
7

8

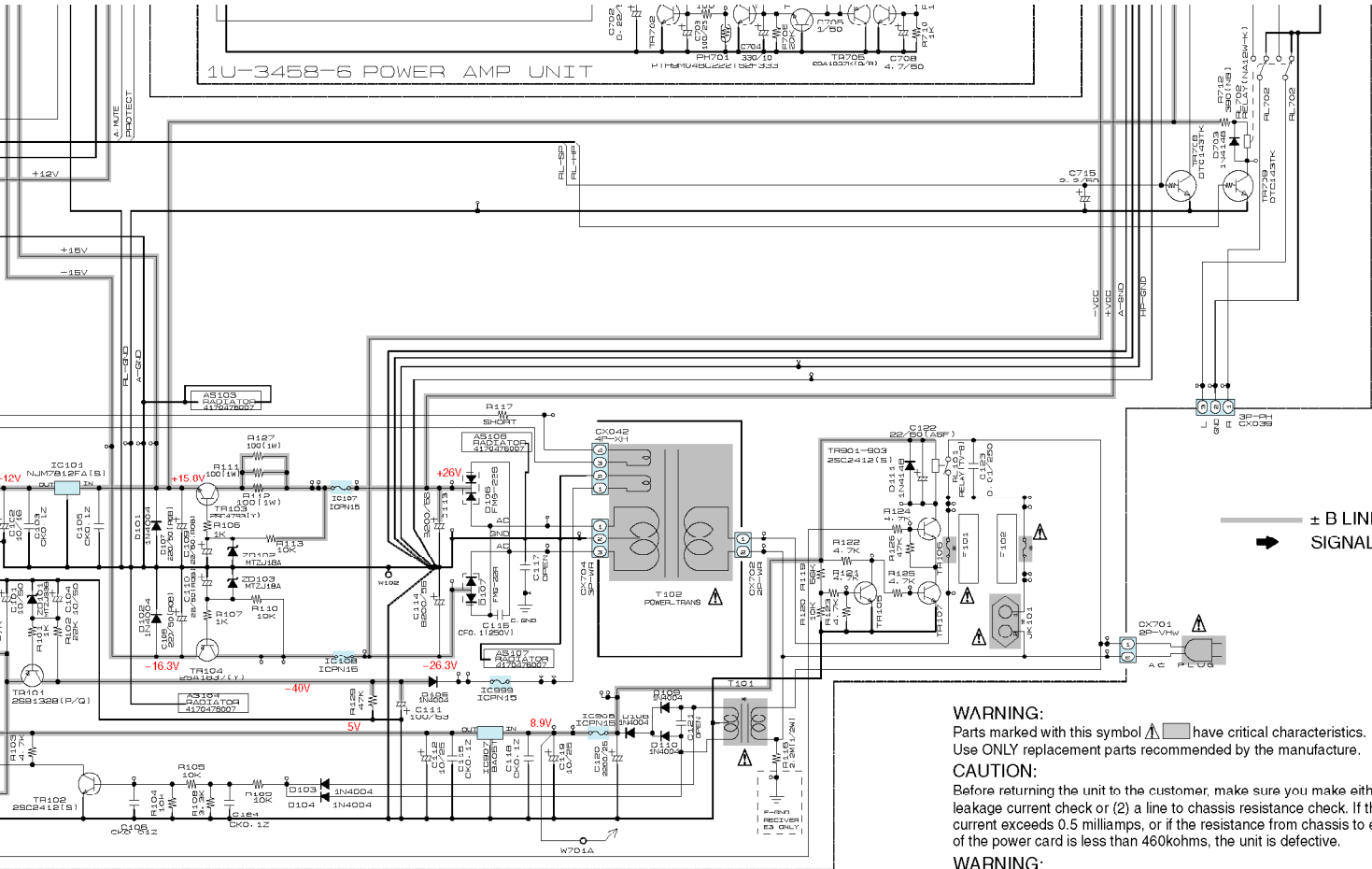
9

10

11



A
B
C
D



NOTICE
 ALL RESISTANCE VALUES IN OHM. k=1,000 OHM M=1,000,000 OHM
 ALL CAPACITANCE VALUES IN MICRO FARAD. P=MICRO-MICRO FARAD
 EACH VOLTAGE AND CURRENT ARE MEASURED AT NO SIGNAL INPUT
 CONDITION.
 CIRCUIT AND PARTS ARE SUBJECT TO CHANGE WITHOUT PRIOR
 NOTICE.

WARNING:
 Parts marked with this symbol \triangle have critical characteristics.
 Use ONLY replacement parts recommended by the manufacturer.

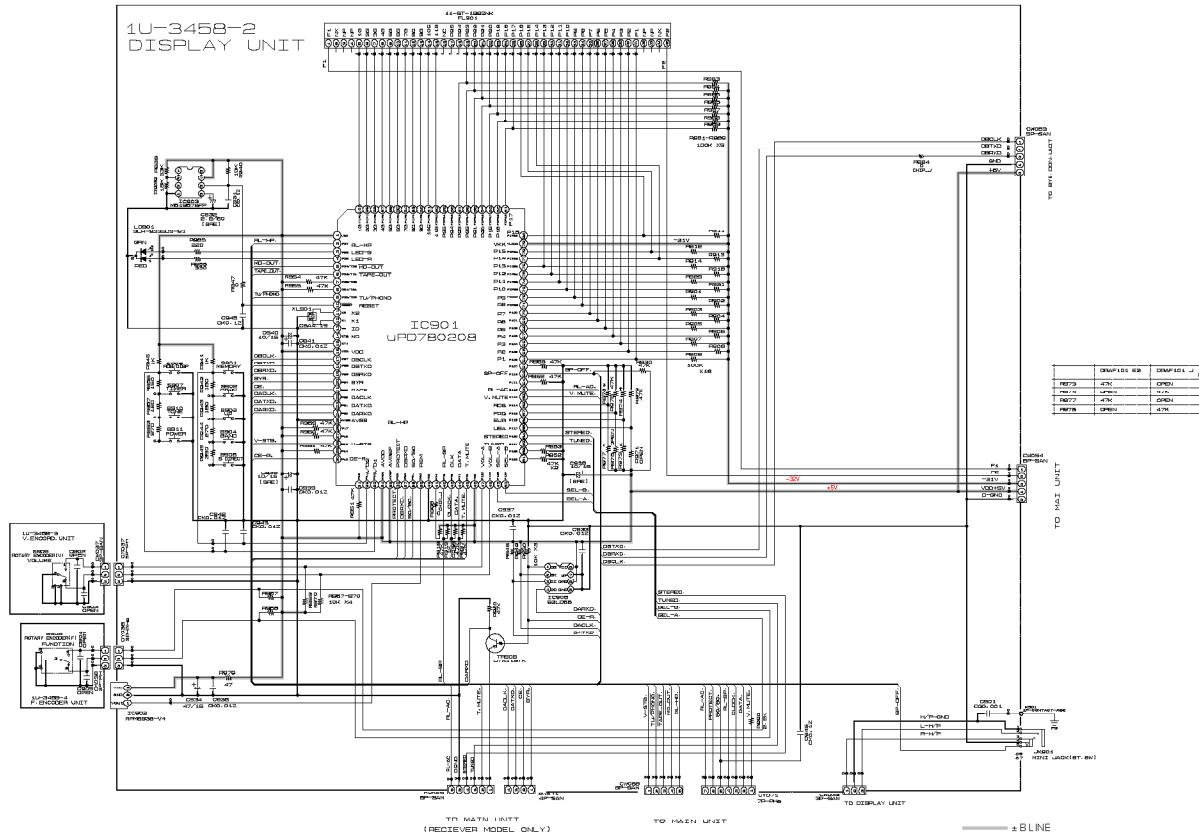
CAUTION:
 Before returning the unit to the customer, make sure you make either (1) a
 leakage current check or (2) a line to chassis resistance check. If the leakage
 current exceeds 0.5 milliamps, or if the resistance from chassis to either side
 of the power card is less than 460kohms, the unit is defective.

WARNING:
 DO NOT return the unit to the customer until the problem is located and
 corrected.


SCHEMATIC DIAGRAMS (1/2)
 1U-3458-1 MAIN UNIT
 1U-3458-5 SYS. CON. UNIT
 1U-3458-6 POWER AMP UNIT

SCHEMATIC DIAGRAMS (2/2)

1 2 3 4 5 6 7 8 9 10 11



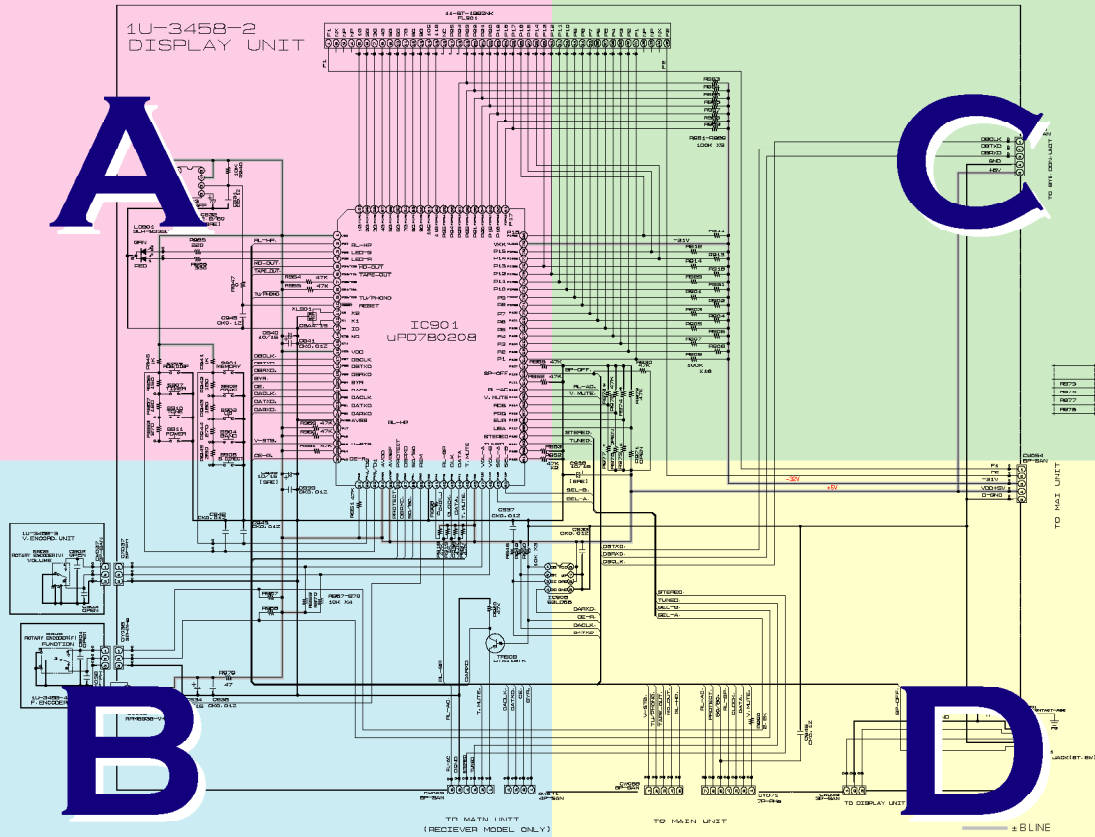
NOTICE
 ALL RESISTANCE VALUES IN OHM, 1k=1,000 OHM Max. 100,000 OHM
 ALL CAPACITANCE VALUES IN MICRO-FARAD, P=PICTO-MICRO-FARAD
 EACH VOLTAGE AND CURRENT ARE MEASURED AT NO SIGNAL INPUT
 CONDITION.
 CIRCUIT AND PARTS ARE SUBJECT TO CHANGE WITHOUT PRIOR
 NOTICE.

WARNING:
 Parts marked with this symbol  have critical characteristics.
 Use ONLY replacement parts recommended by the manufacturer.
CAUTION:
 Before returning the unit to the customer, make sure you make either (1) a
 leakage current check or (2) a line to chassis resistance check. If the leakage
 current exceeds 0.5 milliamperes, or if the resistance from chassis to other side
 of the power cord is less than 400kohms, the unit is defective.
WARNING:
 DO NOT return the unit to the customer until the problem is isolated and
 corrected.

SCHEMATIC DIAGRAMS (2/2)
 1U-3458-2 DISPLAY UNIT
 1U-3458-3 V ENCODER UNIT
 1U-3458-4 F ENCODER UNIT

SCHEMATIC DIAGRAMS (2/2)

1 2 3 4 5 6 7 8 9 10 11




A

C

B

D

NOTICE
 ALL RESISTANCE VALUES IN OHM, 1k=1,000 OHM Max. 100,000 OHM
 ALL CAPACITANCE VALUES IN MICRO FARAD, P=PICTO-MICRO FARAD
 EACH VOLTAGE AND CURRENT ARE MEASURED AT NO SIGNAL INPUT
 CONNECTION.
 CIRCUIT AND PARTS ARE SUBJECT TO CHANGE WITHOUT PRIOR
 NOTICE

WARNING
 Parts marked with this symbol  have critical characteristics.
 Use ONLY replacement parts recommended by the manufacturer.

CAUTION:
 Before returning the unit to the customer, make sure you make either (1) a
 leakage current check or (2) a line to chassis resistance check. If the leakage
 current exceeds 0.5 milliamperes, or if the resistance from chassis to other side
 of the power cord is less than 400kohms, the unit is defective.

WARNING:
 DO NOT return the unit to the customer until the problem is located and
 corrected.

SCHEMATIC DIAGRAMS (2/2)
 1U-3458-2 DISPLAY UNIT
 1U-3458-3 V. ENCODER UNIT
 1U-3458-4 F. ENCODER UNIT

SCHEMATIC DIAGRAMS (2/2)

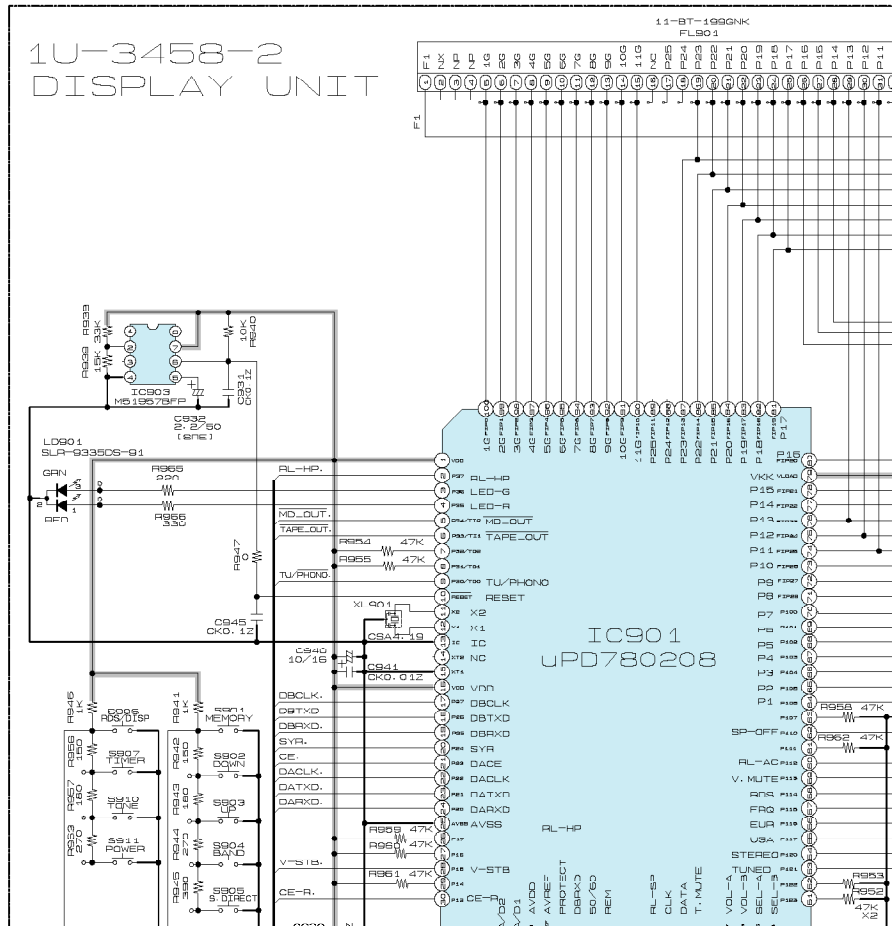
1

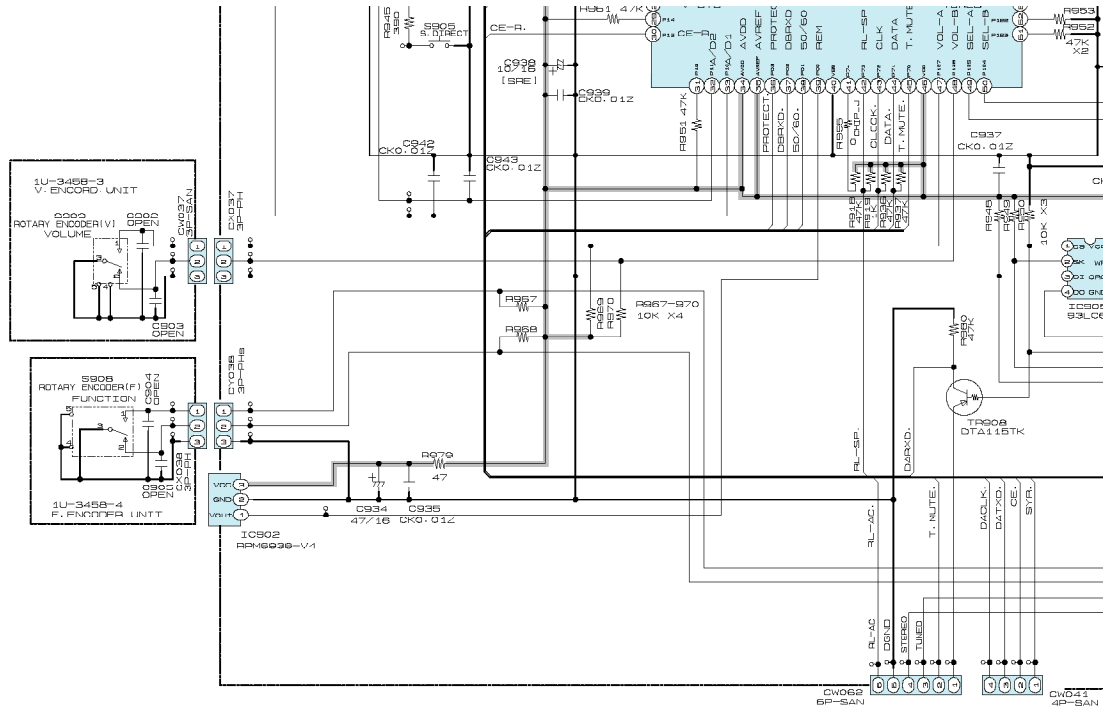
2

3

4

5





NOTICE

ALL RESISTANCE VALUES IN OHM. K=1,000 OHM M=1,000,000 OHM
 ALL CAPACITANCE VALUES IN MICRO FARAD. P=MICRO-MICROFARAD
 EACH VOLTAGE AND CURRENT ARE MEASURED AT NO SIGNAL CONDITION.
 CIRCUIT AND PARTS ARE SUBJECT TO CHANGE WITHOUT NOTICE.

6

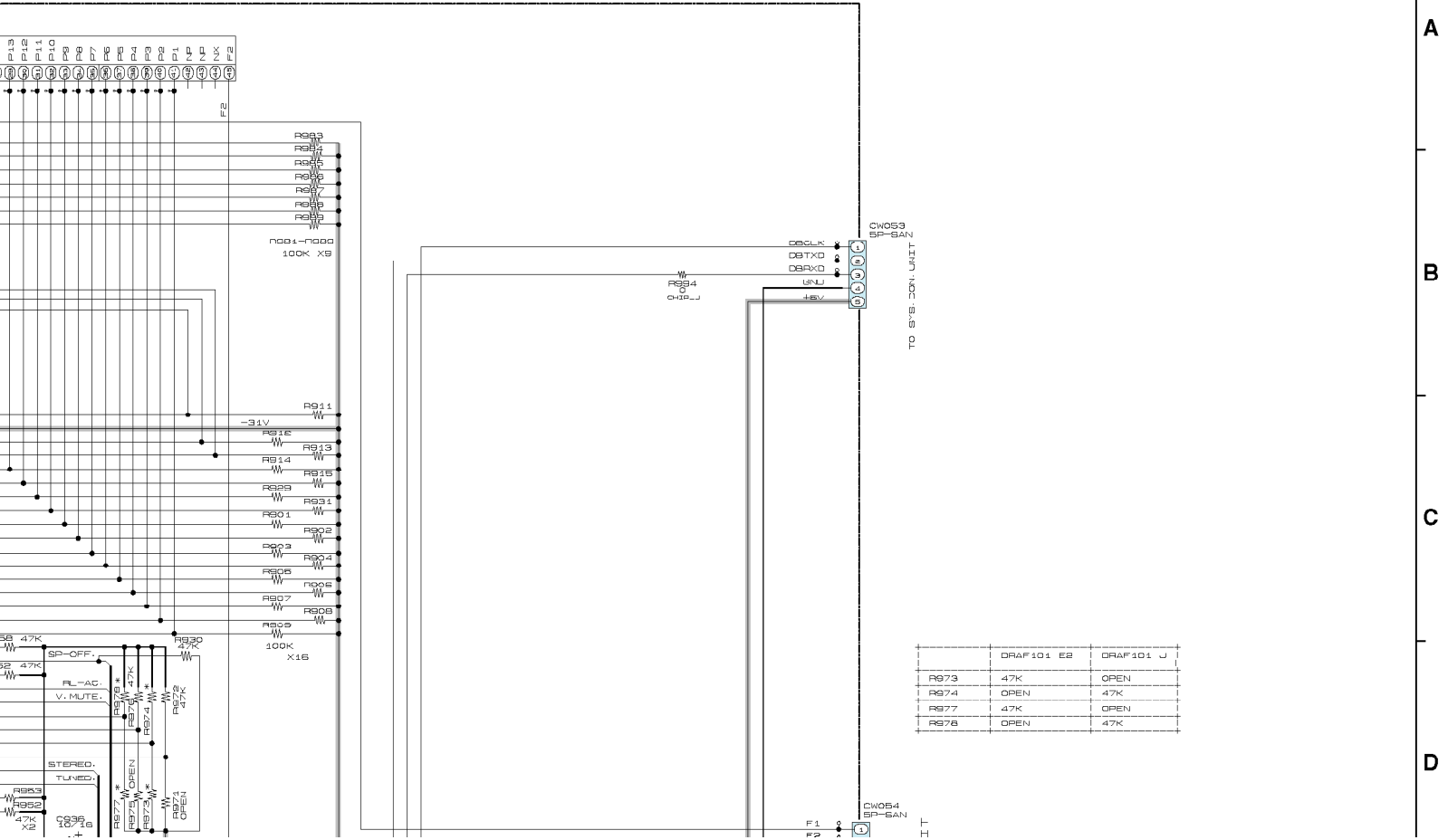
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8

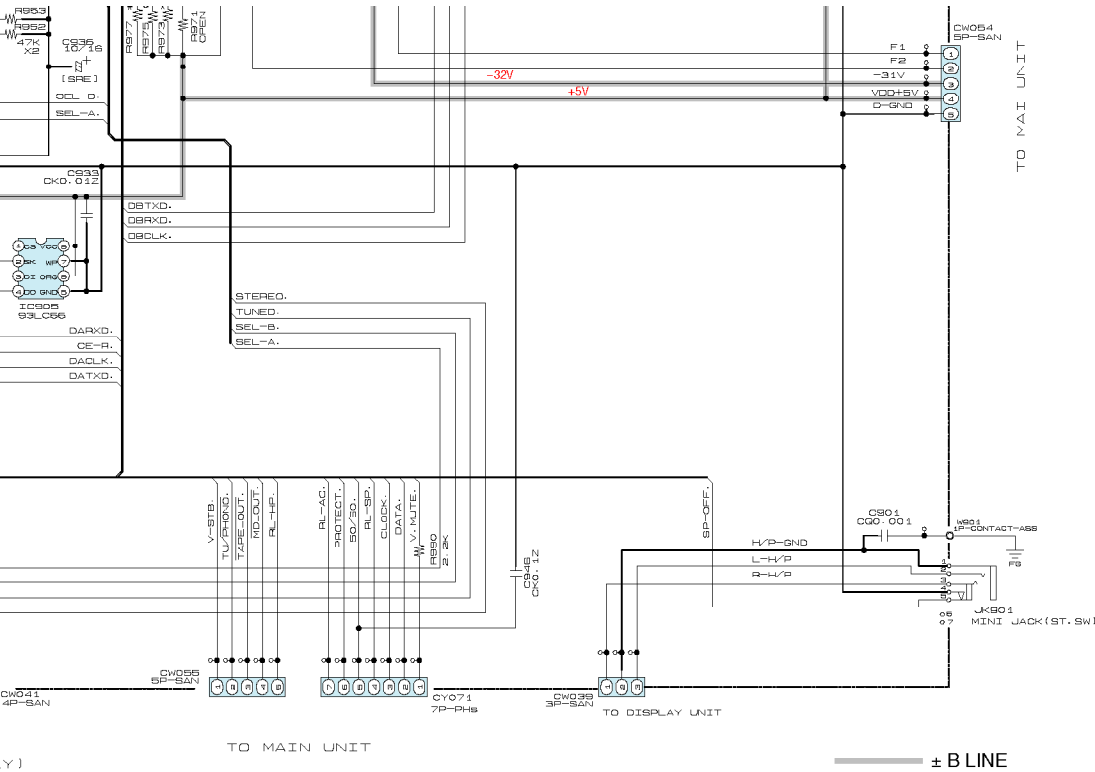
9

10

11



A
B
C
D



0,000 OHM
-MICRO FARAD
SIGNAL INPUT

UT PRIOR

WARNING:

Parts marked with this symbol have critical characteristics. Use ONLY replacement parts recommended by the manufacture.

CAUTION:

Before returning the unit to the customer, make sure you make either (1) a leakage current check or (2) a line to chassis resistance check. If the leakage current exceeds 0.5 milliamps, or if the resistance from chassis to either side of the power card is less than 460kohms, the unit is dofectivo.

WARNING:

DO NOT return the unit to the customer until the problem is located and corrected.

SCHEMATIC DIAGRAMS (2/2)

- 1U-3458-2 DISPLAY UNIT
- 1U-3458-3 V. ENCODER UNIT
- 1U-3458-4 F. ENCODER UNIT

E

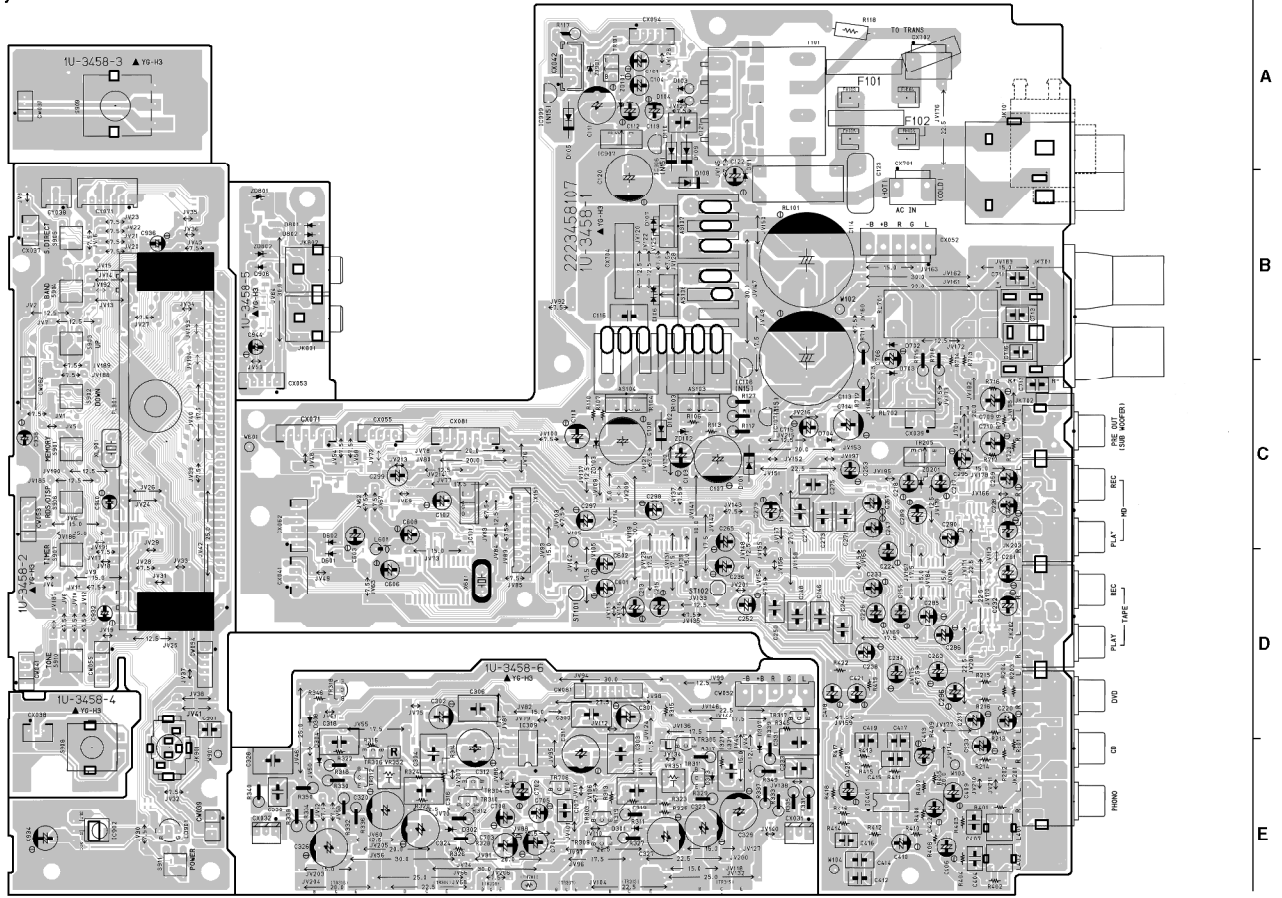
F

G

H

PRINTED WIRING BOARDS

1U-3458 DRA P.W.B. UNIT Ass'y



COMPONENT SIDE

